Ironwood Electronics

SG25 interposer 0.60 mm pitch

Measurement Results

prepared by

Gert Hohenwarter

8/27/2017

Table of Contents

| TABLE OF CONTENTS | 2 |
|-------------------|----|
| | |
| Objective | 3 |
| | |
| METHODOLOGY | 3 |
| Test procedures | 4 |
| | |
| Setup | 4 |
| Measurements | 7 |
| | |
| Time domain | 7 |
| Frequency domain | 9 |
| SUMMARY | 12 |

Objective

The objective of these measurements is to determine the RF performance of a SG25 interposer. A signal pin surrounded by grounded pins is selected for the signal transmission. Measurements in both frequency and time domain form the basis for the evaluation. Parameters to be determined are S-parameters, the propagation delay and the attenuation to 100 GHz.

Methodology

Frequency domain measurements were acquired with a combination of network analyzers (Agilent 8722C to 20 GHz and 8510C to 100 GHz). The instruments are calibrated up to the end of the 0.022" diameter coax probes that are part of the test fixturing. The device under test (DUT) was then mounted to the fixture and the response measured.

Time domain measurements are obtained via Fourier transform from VNA tests.

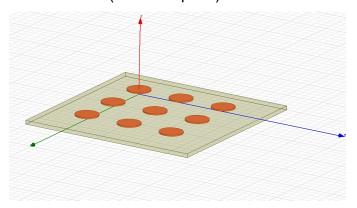
These measurements reveal the type of discontinuities at the interfaces plus contacts.

Test procedures

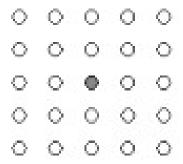
Measurements were taken up to 20 GHz on the 8722C VNA and up to 100 GHz on an 8510C VNA. These measurements are combined in post-processing and fixture deembedding.

Setup

Testing was performed with a test setup that consists of a brass plate that contains the coaxial probes. Contact points as indicated below are established via a 50 um thick PCB with pads of 0.3mm diameter (0.60 mm pitch):



The DUT is aligned and mounted to that plate. The opposite termination is also a metal plate with coaxial probes, albeit in the physical shape of an actual device to be tested or a flat plate with embedded coaxial probes. Measurements are performed for a pin in the center of the array (field), indicated in dark grey:



Signal pins are completely surrounded by ground pins since behavior is similar if they were connected to 50 Ohm driven pins as may be the case in BGA environments.

Figs. 1 and 2 show a typical arrangement base plate and DUT probe:

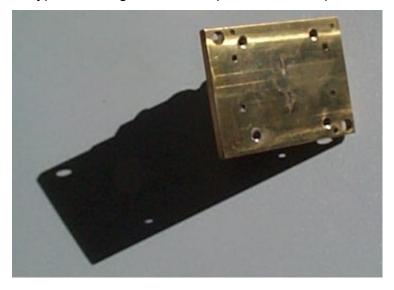


Figure 1 SG25 interposer base plate example



Figure 2 DUT plate example

The SG25 interposer and base plate as well as the DUT plate are then mounted in a test fixture similar to that shown in Fig. 3:

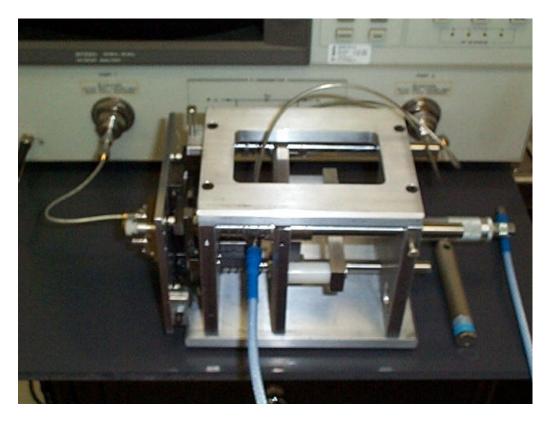


Figure 3 Test fixture example

This fixture provides for independent X,Y and Z control of the components relative to each other. X, Y and angular alignment is established once at the beginning of a test series and then kept constant. Z (depth) alignment is measured via micrometer and is established according to specifications for the particular DUT.

Connections to the VNA are made with high quality coaxial cables.

6

Measurements

Time domain

Then first measurement to be illuminated is the TDR response. It is obtained for a thru measurement from the PCB side through the socket into a 50 Ohm termination on the IC side of the test coupon:

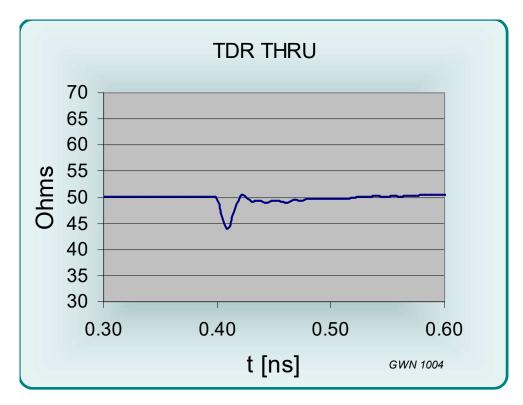


Figure 4 TDR measurement into a 50 Ohm probe (a time shift of 0.4 ns is due to the setup)

The thru TDR measurement shows a capacitive response. The dip corresponds to an impedance of 43.9 Ohms. It should be kept in mind that the impedance recorded here is not as high or as low as actually found in the specimen because of the risetime of the time step, in this case 10 ps. For connections with comparable or shorter electrical lengths this does not allow the peak to reach its full height.

The TDT performance for a step propagating through the contact arrangement was also recorded:

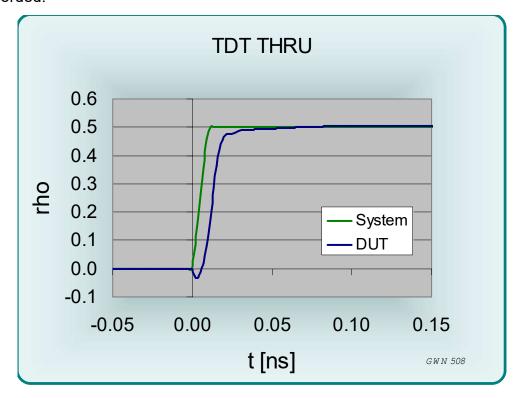


Figure 5 TDT measurement

The TDT measurements for transmission show almost the same risetime from the pin array (10-90% RT = 11.7 ps, the system risetime is 7.8 ps). The added delay at the 50% point is 7.5 ps. There is no signal distortion. If the 20%-80% values are extracted, the risetime is only 5.9 ps vs. 5.9 ps system risetime.

Frequency domain

Network analyzer measurements show an insertion loss as below for the frequency range of 200 MHz to 100 GHz.

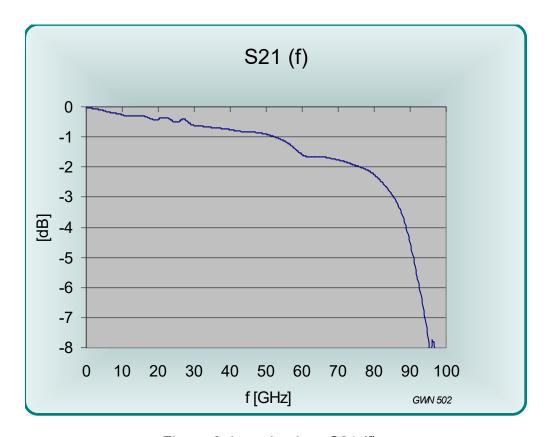


Figure 6 Insertion loss S21 (f)

Insertion loss is less than 1 dB to about 52.4 GHz. The 3 dB point is not reached before 85.4 GHz.

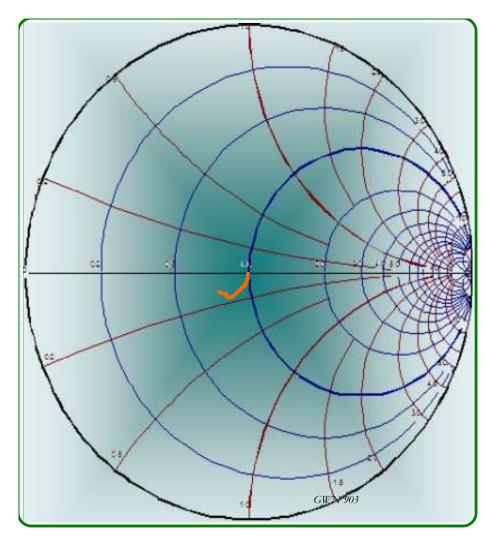


Figure 7 Smith chart for the thru measurement into a 50 Ohm probe

The Smith chart for thru measurements shows a good match at low frequencies. At higher frequencies small reactive components become apparent.

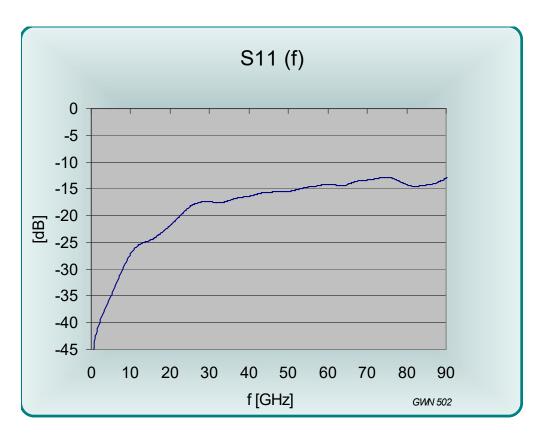


Figure 8 S11 magnitude (f) for the thru measurement into a 50 Ohm probe

Return loss reaches -20 dB at 22.4 GHz for the site.

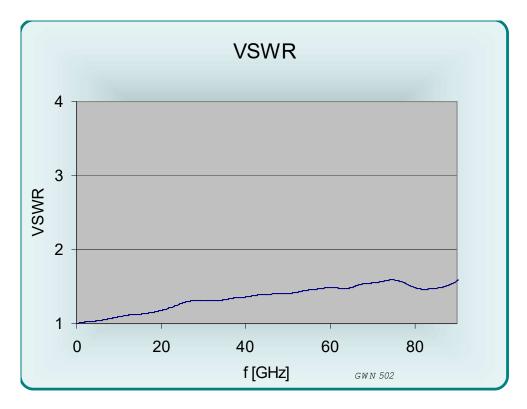


Figure 9 Standing wave ratio VSWR (f) [1 / div.]

The VSWR remains below 2 : 1 to a frequency of >100 GHz.

Summary

| Contact PN: | SG25 | |
|----------------------------------|------|-----|
| Parameter | | |
| S21 (insertion loss) @ -1dB, GSG | 52.4 | GHz |
| S11 (return loss) @ -20 dB, GSG | 22.4 | GHz |
| Impedance | 43.9 | Ω |
| VSWR < 2:1 | >100 | GHz |
| Delay | 7.5 | ps |